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TRANSMITTAL FORM <small>(to be used for all correspondence after initial filing)</small>		Application / Conf. No.	10/082,518 / 5974
		Filing Date	February 22, 2002
		First Named Inventor	L. James Hwang
		Examiner Name	Stacy Whitmore
		Art Unit	2812
Mail Stop:		Patent No.	
Express Mail Receipt No.		Attorney Docket Number	X-1002 US
Total Number of Pages in This Submission		2	

ENCLOSURES (check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Customer Number	24309	Reg. Number 51,959
Attn: Justin Liu	(Customer Number)	
Signature		
Date	July 11, 2003	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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X-1002 US
10/082,518

PATENT
Conf. No.: 5974

IN THE UNITED STATES PATENT OFFICE

Applicants: L. James Hwang et al.
Assignee: Xilinx, Inc.
Title: "Method and System for Integrating CORES in FPGA-Based System-On-Chip (SoC)"

Serial No.: 10/082,518 Filed: February 22, 2002
Examiner: Stacy Whitmore Art Unit: 2812
Docket No.: X-1002 US Conf. No.: 5974

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RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

In response to the Restriction Requirement mailed from the Patent Office on June 30, 2003, Applicants provisionally elect to prosecute Group I: Claims 1-9 with traverse. Applicants hereby cancel Claims 10-15 and 16-18 (Groups II and III) but reserve the right to prosecute Group II (Claims 10-15) and Group III (Claims 16-18) in subsequent divisional applications.

Respectfully submitted,

Justin Liu
Attorney for Applicants
Reg. No. 51,959

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Julie Matthews
Name

Signature